EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2303	baseband with mobile	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2007/02/26 09:31
L4	2477	(726/24,26.ccls.) (714/716,725,727. ccls.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/26 09:30
L5	1	1 and 4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/26 09:30
L6	96	(baseband near3 chip) with mobile	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2007/02/26 09:31
L7	89	6 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/26 09:31
L8	89	((baseband near3 chip) with mobile) and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/26 09:32
S1	1149	726/26.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 09:38
S2	624	S1 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/26 09:31
S3	5	S2 and (JTAG (joint adj test adj action adj group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 09:52
S4	1	10/706365.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:05
S5	0	("2005/0099832").URPN.	USPAT	ADJ	ON	2007/02/20 09:51
S6	5	S2 and (JTAG (joint adj3 test adj3 action adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:18
S7	33483	(JTAG (joint adj3 test adj3 action adj3 group) PLD (programmable near3 logic near3 device))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:02
S8	5494	(JTAG (joint adj3 test adj3 action adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:19
S9	106601	"455"/$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:01

S10	59	S8 and S9	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2007/02/26 09:25
S11	28938	PLD (programmable near3 logic near3 device)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:02
S12	26	S10 and S11	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:02
S13	5	S2 and (JTAG (joint adj3 test adj3 (action access) adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:18
S14	5495	(JTAG (joint adj3 test adj3 (action access) adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:24
S15	1299	(JTAG (joint adj3 test adj3 (action access) adj3 group)) near3 port	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:24
S16	0	S1 and S15	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:25
S17	296	S11 and S15	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:25
S18	1435	(IC (integrated circuit)) near5 ((test$3 jtag (joint near3 action)) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 19:00
S19	16267	(prevent$3 near3 (reprogram$4 modification modif$4 reconfigur$5))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 10:37
S20	9	S18 and S19	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 11:07
S21	1705	Xilinx.as.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 11:08
S22	1176	S21 and S11	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 11:08
S23	5	S18 and S22	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 11:21
S24	1	08/512796.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 13:17

S25	1	08/670916.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 11:51
S26	16	("5764076").URPN.	USPAT	ADJ	ON	2007/02/20 11:30
S27	1	08/670472.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 13:11
S28	1	08/533412.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 13:18
S29	12	("5689516").URPN.	USPAT	ADJ	ON	2007/02/20 14:30
S30	0	("7058856").URPN.	USPAT	ADJ	ON	2007/02/20 15:00
S31	2144	inhibit near3 (bit nand gate circuitry) same (one near3 time near3 programmable register)	USPAT	OR	ON	2007/02/23 16:03
S32	682	(inhibit near3 (bit circuitry)) same (one near3 time near3 programmable register)	USPAT	OR	ON	2007/02/20 15:05
S33	261	(inhibit near3 bit) with (one near3 time near3 programmable register)	USPAT	OR	ON	2007/02/20 15:04
S34	4	S18 and S33	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 15:03
S35	0	(inhibit near3 bit) with (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2007/02/20 15:04
S36	0	(inhibit near3 (bit circuitry)) same (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2007/02/20 15:06
S37	50	(one near3 time near3 programmable near3 register)	USPAT	OR	ON	2007/02/20 18:30
S38	4	S18 and S33	USPAT	OR	ON	2007/02/20 15:08
S39	1	S18 and S37	USPAT	OR	ON	2007/02/20 15:08
S40	4614	((one near3 time near3 programmable)(one near3 time)(non near3 programmable)eprom) near3 register	USPAT	OR	ON	2007/02/20 20:29
S41	43	S15 and S40	USPAT	OR	ON	2007/02/20 18:33
S42	0	(inhibit near3 (bit nand gate circuitry)) with ((test$3 JTAG) near3 port) with (enabl$4 desabl$4)	USPAT	OR	ON	2007/02/20 18:37
S43	9211	(inhibit near3 (bit nand gate circuitry))	USPAT	OR	ON	2007/02/20 18:47

S44	951	((test$3 JTAG) near3 port) same enabl$4 desabl$4	USPAT	OR	ON	2007/02/20 19:15
S45	0	S43 same S44	USPAT	OR	ON	2007/02/20 18:39
S46	25	S43 and S44	USPAT	OR	ON	2007/02/20 18:39
S47	0	(inhibit near3 (bit nand gate circuitry)) near3 (modifiable)	USPAT	OR	ON	2007/02/20 18:47
S48	0	(inhibit near3 (bit nand gate circuitry)) with (modifiable)	USPAT	OR	ON	2007/02/20 18:48
S49	0	(inhibit near3 (bit nand gate circuitry)) same (modifiable)	USPAT	OR	ON	2007/02/20 18:48
S50	187	(inhibit near3 (bit nand gate circuitry)) same (modif$5)	USPAT	OR	ON	2007/02/20 18:49
S51	60	(inhibit near3 (bit nand gate circuitry)) with (modif$5)	USPAT	OR	ON	2007/02/20 19:11
S52	19	(inhibit near3 (bit nand gate circuitry)) near3(modif$5)	USPAT	OR	ON	2007/02/20 18:49
S53	0	S4 and S50	USPAT	OR	ON	2007/02/20 18:49
S54	0	S44 and S50	USPAT	OR	ON	2007/02/20 18:49
S55	50	(inhibit near3 (bit nand gate circuitry)) near3(modif$5 updat$3)	USPAT	OR	ON	2007/02/20 18:50
S56	1	((inhibit near3 (bit nand gate circuitry)) near3(modif$5 updat$3)) same configur$5	USPAT	OR	ON	2007/02/20 18:51
S57	4	((inhibit near3 (bit nand gate circuitry)) with (modif$5 updat$3)) same configur$5	USPAT	OR	ON	2007/02/20 18:51
S58	0	S18 and S50	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/20 19:00
S59	222	(inhibit near3 (bit nand gate circuitry signal$1)) with (modif$5)	USPAT	OR	ON	2007/02/23 11:27
S60	0	S44 and S55	USPAT	OR	ON	2007/02/20 19:11
S61	0	S18 and S55	USPAT	OR	ON	2007/02/20 19:11
S62	20476	(inhibit near3 (bit nand gate circuitry signal$1))	USPAT	OR	ON	2007/02/20 19:14
S63	2061	((test$3 JTAG) near3 port) same (enabl$4 allow$3)	USPAT	OR	ON	2007/02/21 13:41
S64	113	S62 and S63	USPAT	OR	ON	2007/02/20 19:16
S65	1050	((test$3 JTAG) near3 port) with(enabl$4 allow$3)	USPAT	OR	ON	2007/02/20 20:11
S66	528	((test$3 JTAG) near3 port) with(enabl$4 disabl$3)	USPAT	OR	ON	2007/02/20 20:11

S67	53	((test$3 JTAG) near3 port) with (enabl$4 and disabl$3)	USPAT	OR	ON	2007/02/20 20:21
S68	4	(((test$3 JTAG) near3 port) with enabl$4) same inhibit	USPAT	OR	ON	2007/02/20 20:22
S69	3	("6523099").URPN.	USPAT	ADJ	ON	2007/02/20 20:25
S70	505	S40 and (inhibit near3 (circuitry signal circuit gate))	USPAT	OR	ON	2007/02/20 20:32
S71	1	(S40 and (inhibit near3 (circuitry signal circuit gate))).ab.	USPAT	OR	ON	2007/02/20 21:14
S72	0	S70 and (baseband near3 chip)	USPAT	OR	ON	2007/02/20 20:29
S73	11	S70 and baseband	USPAT	OR	ON	2007/02/20 20:29
S74	4	S40 with (inhibit near3 (circuitry signal circuit gate))	USPAT	OR	ON	2007/02/20 20:34
S75	942	726/24,26.ccls. or 714/716,725.ccls.	USPAT	OR	ON	2007/02/20 20:35
S76	0	S75 and ((inhibit near3 circuitry) same (((test$3 JTAG) near3 port))with enabl$3)	USPAT	OR	ON	2007/02/20 20:37
S77	0	S75 and ((inhibit near3 circuitry) and(((test$3 JTAG) near3 port))with enabl$3)	USPAT	OR	ON	2007/02/20 20:37
S78	4	((inhibit near3 circuitry) and(((test$3 JTAG) near3 port))with enabl$3)	USPAT	OR	ON	2007/02/20 20:39
S79	4	((inhibit near3 circuitry) and(((test$3 JTAG) near3 port))same enabl$3)	USPAT	OR	ON	2007/02/21 14:36
S80	207	(inhibit near3 (circuitry gate signal)) and ((test$3 JTAG) near3 port)	USPAT	OR	ON	2007/02/20 20:40
S81	11	("20050039039" | "4698750" | "5689516" | "5737760" | "5819025" | "6145122" | "6243842" | "6523099" | "6662314" | "6711684" | "6769065").PN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/02/20 20:41
S82	83	("4698750").URPN.	USPAT	ADJ	ON	2007/02/20 20:49
S83	25	(johannes near3 becker).in.	USPAT	OR	ON	2007/02/20 21:17
S84	1132	(agere near3 inc).as.	USPAT	OR	ON	2007/02/20 21:19
S86	12	(johannes adj becker).in.	USPAT	OR	ON	2007/02/20 21:16
S87	25	(johannes near2 becker).in.	USPAT	OR	ON	2007/02/20 21:17
S88	1825	(agere near3 systems).as.	USPAT	OR	ON	2007/02/20 21:19
S89	9	S88 and JTAG	USPAT	OR	ON	2007/02/20 21:19

S90	3580	(mobile wireless pda mda (mp3 near3 player) (set near top)) same (((one single) near3 time near3 program$5) eprom (non near3 programmable))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:10
S91	27	((mobile wireless pda mda (mp3 near3 player) (set near top)) same (((one single) near3 time near3 program$5) eprom (non near3 programmable))).ab.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:13
S92	17	((mobile wireless pda mda (mp3 near3 player) (set near top)) same (((one single) near3 time near3 program$5)(non near3 programmable))).ab.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:22
S93	203	((mobile pda mda (mp3 near3 player)) same (((one single) near3 time near3 program$5)(non near3 programmable)))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:38
S94	341	(((one single) near3 time near3 program$5)(non near3 programmable)) near3 register	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:38
S95	456683	(mobile pda mda (mp3 near3 player))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:39
S96	6	S94 same S95	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:40
S97	57	S94 and S95	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:41
S98	0	S97 and (((test$3 JTAG) near3 port) same enabl$4)	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2007/02/21 13:43
S99	0	S97 and ((test$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2007/02/21 13:43
S100	1	S97 and (((test$3 JTAG) near3 port) same enabl$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:43
S101	0	("2005/0099832").URPN.	USPAT	ADJ	ON	2007/02/21 13:43
S102	4	S97 and (((test$3 JTAG) near3 port) and enabl$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:44
S103	4	S97 and ((test$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:45

S10 4	24	S94 and ((test$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/21 13:45
S10 5	14	("5838901").URPN.	USPAT	ADJ	ON	2007/02/21 13:51
S10 6	16	("5764076").URPN.	USPAT	ADJ	ON	2007/02/21 13:56
S10 7	11	("20050039039" | "4698750" | "5689516" | "5737760" | "5819025" | "6145122" | "6243842" | "6523099" | "6662314" | "6711684" | "6769065").PN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/02/21 13:57
S10 8	1	"5199032".pn.	USPAT	OR	ON	2007/02/21 14:36
S10 9	38	("5199032").URPN.	USPAT	ADJ	ON	2007/02/21 14:57
S11 0	6987	Ericsson.as.	USPAT	ADJ	ON	2007/02/21 14:57
S11 1	3	S110 and (jtag)	USPAT	ADJ	ON	2007/02/21 14:59
S11 2	0	xilinx.as. and (JGAT)	USPAT	ADJ	ON	2007/02/21 15:01
S11 3	1461	xilinx.as.	USPAT	ADJ	ON	2007/02/21 15:01
S11 4	108	S113 and JTAG	USPAT	ADJ	ON	2007/02/21 15:13
S11 5	0	S113 and (inhibit JTAG)	USPAT	ADJ	ON	2007/02/21 15:01
S11 6	147	S113 and (inhibit JTAG)	USPAT	OR	ON	2007/02/21 15:01
S11 7	2	S113 and (inhibit and JTAG)	USPAT	OR	ON	2007/02/21 15:01
S11 8	740	714/30.ccls.	USPAT	ADJ	ON	2007/02/21 15:13
S11 9	176	S118 and JTAG	USPAT	ADJ	ON	2007/02/21 15:13
S12 0	105	S119 and tap	USPAT	ADJ	ON	2007/02/21 15:13
S12 1	22	S120 and inhibit	USPAT	ADJ	ON	2007/02/22 11:06
S12 2	1149	726/26.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 11:22

S12 3	624	S122 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 13:45
S12 4	8565	((test$3 JTAG) near3 port) and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 11:29
S12 5	222	(inhibit near3 (bit nand gate circuitry signal$1)) with (modif$5)	USPAT	OR	ON	2007/02/23 11:27
S12 6	0	S125 and S124	USPAT	OR	ON	2007/02/23 11:27
S12 7	20476	(inhibit near3 (bit nand gate circuitry signal$1))	USPAT	OR	ON	2007/02/23 11:27
S12 8	236	S124 and S127	USPAT	OR	ON	2007/02/23 11:28
S12 9	1234	(port near3 access near3 circuitry) (tap near3 controller)	USPAT	OR	ON	2007/02/23 11:28
S13 0	53	S128 and S129	USPAT	OR	ON	2007/02/23 11:28
S13 1	56	((test$3 JTAG) near3 port) same (port near3 access near3 circuitry)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 13:44
S13 2	7	S130 and S131	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 11:29
S13 3	2	("6381721").URPN.	USPAT	ADJ	ON	2007/02/23 13:42
S13 5	400	714/727.ccls.	USPAT	ADJ	ON	2007/02/23 13:42
S13 6	252	S135 and ((test$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 13:44
S13 7	12	S127 and S136	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 13:45
S13 8	12	S137 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 13:45
S13 9	12	S138 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/02/23 13:48
S14 0	0	inhibit near3 (bit nand gate circuitry) with (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2007/02/23 16:03
S14 1	0	inhibit near3 (bit nand gate circuitry) same(one near3 time near3 programmable near3 register)	USPAT	OR	ON	2007/02/23 16:04

S14 2	0	inhibit same(one near3 time near3 programmable near3 register)	USPAT	OR	ON	2007/02/23 16:04
S14 3	0	inhibit and (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2007/02/23 18:40
S14 4	0	loopback near3 input near3 output near3 pin$1	USPAT	OR	ON	2007/02/23 18:41
S14 5	1	loop near3 back near3 input near3 output near3 pin$1	USPAT	OR	ON	2007/02/23 18:45
S14 6	1	"6625472".pn.	USPAT	OR	ON	2007/02/23 18:43
S14 7	99	loop near3 back near3 pin$1	USPAT	OR	ON	2007/02/23 18:45
S14 8	35	loopback near3 pin$1	USPAT	OR	ON	2007/02/23 18:45